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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/658,195	09/10/2003	Kenichiro Uda	56937-089	3242

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McDERMOTT, WILL & EMERY
600 13th Street, N.W.
Washington, DC 20005-3096

EXAMINER

ROSSOSHEK, YELENA

ART UNIT	PAPER NUMBER
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2825

DATE MAILED: 10/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/658,195

Applicant(s)

UDA, KENICHIRO

Examiner

Helen Rossoshek

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 September 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>9/10/03, 1/7/05, 3/1</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This office action is in response to the Application 10/658,195 filed 09/10/2003.

2. Claims 1-12 are pending in the Application.

Claim Objections

3. Claims 2-12 are objected to because of the following informalities: there is insufficient antecedent basis issue in the claims 2-12.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 1-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Kudou et al. (US Patent 6,617,622).

With respect to claim 1 Kudou et al. teaches a structure of a power supply path utilized in the design of an integrated circuit (abstract), wherein a plurality of outgoing lines branch off from each of main lines of respective power supply paths as shown on the Fig. 8, wherein two main lines 17-1 and 17-2 disposed on two opposite sides, having plurality of branches 17-1a, 17-1b, 17-1c and 17-2a, 17-2b, 17-2c (col. 2, ll.61-

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65) on a power supply side of a high potential and on a power supply side of a low potential as shown on the Fig. 16, which details the Fig. 8, wherein, for example two power main lines 17-1 and 17-2 are represented by (31a) V_{cc2} and (31b) V_{ss2} respectively and V_{cc2} is the power supply side of a high potential and V_{ss2} is the power supply side of a low potential (or ground) (col. 11, ll.26-28, ll.41-43) and pitches between adjacent outgoing lines of the plurality of branched outgoing lines are set so as to be equal to each other as shown on the Figs. 8, 15 and 16, wherein two main lines 17-1 and 17-2 arranged between pad groups 12-1 and 12-2 (col. 11, ll.8-10) and disposed in the branch form, wherein pitches between branches 17-1a and 17-1b and 17-1c are equal as well as pitches between 17-2a and 17-2b and 17-2c.

With respect to claims 2-12 Kudou et al. teaches:

Claim 2: wherein branching positions of the plural outgoing lines of the power supply path on the power supply side of the high potential correspond to branching positions of the plural outgoing lines of the power supply path on the power supply side of the low potential in the longitudinal direction of the power supply paths as shown on the Fig. 8, wherein two main lines 17-1 and 17-2 disposed on two opposite sides, having plurality of branches 17-1a, 17-1b, 17-1c and 17-2a, 17-2b, 17-2c positioned in the longitudinal direction of the power supply paths (col. 2, ll.61-65);

Claims 3 and 4: wherein lengths of the respective outgoing lines are set so as to be equal to each other in both the power supply paths on the power supply sides of the high potential and the low potential, respectively as shown on the Fig. 8, wherein two main lines 17-1 and 17-2 disposed on two opposite sides, having plurality of branches

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17-1a, 17-1b, 17-1c and 17-2a, 17-2b, 17-2c positioned in the longitudinal direction of the power supply paths and having the same length (col. 2, ll.61-65);

Claims 5 and 6: wherein the lengths of the plural outgoing lines of the power supply path on the power supply side of the high potential are set so as to be longer than the lengths of the plural outgoing lines of the power supply path on the power supply side of the low potential within the ability of the method to give a freedom of the arrangement and positioning of the circuits, which are fed by power supply lines 17-1 and 17-2 (col.11, ll.17-20), wherein the configuration of the wiring may be modified (simplified) depending on the position of the circuit for which the power supply is provided (col. 11, ll.20-25), additionally as well known in the art the length and width of the power supply lines can be calculated and recalculated, for example as disclosed in the US Patent 6,405,346 (col. 7, ll.63-67; col. 8, ll.59-63);

Claims 7-12: wherein widths of the respective plural outgoing lines are equal to each other and set so as to be smaller than distances between the adjacent outgoing lines of both the power supply paths on the power supply sides of the high potential and the low potential, respectively within the ability of the method to give a freedom of the arrangement and positioning of the circuits, which are fed by power supply lines 17-1 and 17-2 (col.11, ll.17-20), wherein the configuration of the wiring may be modified (simplified) depending on the position of the circuit for which the power supply is provided (col. 11, ll.20-25), additionally as well known in the art the length and width of the power supply lines can be calculated and recalculated, for example as disclosed in the US Patent 6,405,346 (col. 7, ll.63-67; col. 8, ll.59-63).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Helen Rossoshek whose telephone number is 571-272-1905. The examiner can normally be reached on 7:00-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner
Helen Rossoshek
AU 2825



**LEIGH M. GARBOWSKI
PRIMARY EXAMINER**